# PRINCO DDR3-1800 user guide and testing for ASUS M4A88TD-V Motherboard

### AMD 965 3.40G



### Part I: Standard test

It's the easiest way to enjoy overclock benefit by using PRINCO DDR3-1800 DIMM board

How to use?

- 0. Clear BIOS to mainboard initial setting
- 1. Enter BIOS setup and [Ai Tweaker] menu
- 2. Enter [Ai Overclock Tuner] item and select [Manual]
- 3. Select [CPU/HT Reference Clock (MHz)] item <sup>,</sup> and increase to higher Base clock rate (ex:225). Then select [DRAM Frequency] item <sup>,</sup> and set the DDR3 memory to higher clock rate (ex:1800MHz). Don't forget setting [CPU Ratio] item to suitable ratio (ex:15.0)

(In this case we only focus on memory over clocking, not CPU)

4. Select[DRAM Voltage] item , and set the value to [1.60500]

Step 1 : Select " Ai Tweaker "	
Step 2 : CPU Overclocking	Set [Manual]
Step 3 : CPU/HT Reference Clock (MHz)	Set [225]
Step 4 : DRAM Frequency	Set [1800MHz]
Step 5 : CPU Ratio	Set [15.0]
Step 6 : DRAM Voltage	Set 11.605001
	Setun Uersion 1494
Main II Turalan Advanced Power Boot	Tools Exit
Step 1	
CPU_OverClocking [Manual] Step 2	
OC From CPU Level Up [Auto]	Min = 1.20000U
CPU Ratio [15.0] Step 5	Max = 2.445000(*)
CPU/HI Reference Clock (MHz) [225] Step 3	Interval = 0.015000
PCIE Frequency [100]	Standard = $1.500000$
Chu/MP Energy (Outo)	+/- : Kalse/Keduce
UT Link Snord IOutol	
CPU Bosetor (Frabled)	
Enhanced iGPU SneedSten [Auto]	
GPIL Power Sauing Mode [Bisabled]	
DC Tuner Iltilitu	
DRAM Tining configuration	↔ Select Screen
▶ DRAM Driving Configuration	14 Select Item
******* Please key in numbers directly! *******	F1 General Help
CPU & NB Voltage Mode [Offset]	F10 Save and Exit
CPU Offset Voltage 1.400 [Auto]	ESC Exit
CPU/NB Offset Voltage 1.100 [Auto]	
CPU UDDA Voltage 2.500 [Auto]	
DRAM Voltage 1.598 [1.60500] Step 6	•
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## 5. Enter [DRAM Timing Configuration] item

Main	Ai Tweaker	Advanced	Power	Boot	Too	ls	Exit
OC Fro	m CPU Level Up		[Auto]				
<b>CPU</b> Ra	tio		[15.0]				
CPU/	HT Reference C	lock (MHz)	[225]				
PCIE F	requency		[100]				
DRAM F	requency		[1800MHz]				
CPU/NB	Frequency		[Auto]				
HT Lin	k Speed		[Auto]				
<b>GPU Bo</b>	oster		[Enabled]				
Enha	nced iGPU Speed	lStep	[Auto]				
GPU	Power Saving Mo	ode	[Disabled]				
OC Tun	er Utility						
<ul> <li>DRAM</li> </ul>	Tining config	iration					
DRAM	Driving Config	juration				4-9	Select Screen
*****	* Please key in	n numbers d	lirectly! **	*****		11	Select Item
CPU 8	NB Voltage Mode	B	[Offset]			Enter	Go to Sub Screen
CPU	Offset Voltage	1.400	[Auto]			F1	General Help
CPU/	NB Offset Volta	age 1.100	[Auto]			F10	Save and Exit
CPU UD	DA Voltage	2.500	[Auto]			ESC	Exit
DRAM U	oltage	1.590	1.60500				
HT Vol	tage	1.200	[Auto]				

6. set [DRAM CAS# Latency] item to [8 CLK]

set [DRAM RAS# to CAS# Delay] item to [9 CLK]

set [DRAM RAS# PRE Time] item to [8 CLK]

set [DRAM RAS# ACT Time] item to [27 CLK]

set [DRAM Command Rate] item to [1T]

then return to previous to [Ai Overclock Tuner] menu

Step 1: DRAM CAS# Latency			Set [8 CLK]
DRAM RAS# to CAS# D	Delay		Set [9 CLK]
DRAM RAS# PRE Time			Set [8 CLK]
DRAM RAS# ACT Time			Set [27 CLK]
Step 2 : DRAM Command Rate			Set [17]
Ai Tweaker	D-V EVO/USB	3 BIOS Setu	p Version 1404
DRAM Timing configuration			DRAM Command Rate:
DRAM CAS# LatencyDRAM RAS# to CAS# DelayDRAM RAS# PRE TimeDRAM RAS# ACT TimeDRAM RAS# ACT TimeDRAM READ to PRE TimeDRAM READ to PRE TimeBRAM Row Cycle TimeBRAM WRITE Recovery TimeDRAM RAS# to RAS# Delay	9 18 CLK1 9 19 CLK1 9 18 CLK1 4 127 CLK1 4 IAutol 13 IAutol 8 IAutol 4 IAutol	Step 1	control signals are driven for one MEMCLK cycle. 2T: One additional MEMCLK of setup time is provided on all DRAM address and control signals.
DRAM READ TO URITE Delay DRAM WRITE TO READ Delay (DD) DRAM WRITE TO READ Delay (SD) DRAM WRITE TO WRITE Timing DRAM READ TO READ Timing DRAM REF Cycle Time 11 DRAM Refresh Rate 7. DRAM Command Rate 1	8       [Auto]         2       [Auto]         5       [Auto]         4       [Auto]         4       [Auto]         0       [Auto]         8       [Auto]         1       [Auto]	Step 2	<ul> <li>↔ Select Screen</li> <li>↑↓ Select Item</li> <li>↔ Change Option</li> <li>F1 General Help</li> <li>F10 Save and Exit</li> <li>ESC Exit</li> </ul>

## 7. Save BIOS changes [F10] and exit

M4A88TD-V EVO/USB3 BIOS Setur Ai Tweaker	Version 1404
DRAM Timing configuration	DRAM Command Rate:
DRAM CAS# Latency 9 18 CLKI	control signals are
DRAM RAS# to CAS# Delay 9 [9 CLK]	driven for one MEMCLK
DRAM RAS# PRE Time 9 18 CLKJ	cycle.
DRAM RAS# ACT Time 24 [27 CLK]	2T: One additional
DRAM READ to PR.	of setun time
DRAIT READ CO TA	or seeup erne
DRAM Row Cycle	ded on all
DRAM Row Cycle DRAM WRITE Reco Save configuration changes and exi	it now? dress and
DRAM Row Cycle DRAM WRITE Reco DRAM RASH to RA	it now? ded on all signals.
DRAM Row Cycle DRAM WRITE Reco DRAM RASH to RA DRAM READ To UR	Ided on all it now? iress and signals.
DRAM ROW Cycle DRAM WRITE Reco DRAM RASH to RA DRAM READ To VR DRAM READ To VR DRAM WRITE To R	it now?
DRAM ROW Cycle DRAM WRITE Reco DRAM RASH to RA DRAM READ To WR DRAM WRITE To R DRAM WRITE To R DRAM WRITE To READ Delay(SD) 5 [Auto]	Ided on all it now? Ided on all iress and signals. Iect Screen
DRAM ROW Cycle DRAM WRITE Reco DRAM RASH to RA DRAM READ To VR DRAM WRITE To R DRAM WRITE To READ Delay (SD) 5 [Auto] DRAM WRITE To WRITE Timing 4 [Auto]	it now? Ided on all iress and signals. Iect Screen Ided on all iress and signals. Iect Screen Ided on all iress and signals.
DRAM Row Cycle DRAM WRITE Reco DRAM WRITE Reco DRAM RASH to RA DRAM READ To WR DRAM WRITE To R DRAM WRITE To READ Delay (SD) 5 [Auto] DRAM WRITE To WRITE Timing 4 [Auto] DRAM WRITE To READ Timing 4 [Auto]	Ided on all Ided on all Incess and Signals. Iect Screen It Select Item +- Change Option F1 General Help
DRAM ROW Cycle DRAM WRITE Reco DRAM WRITE Reco DRAM RASH to RA DRAM READ To UR DRAM WRITE To RE DRAM WRITE TO WRITE Timing 4 [Auto] DRAM READ To READ Timing 4 [Auto] DRAM READ To READ Timing 4 [Auto]	Ided on all it now? Ided on all iress and signals. Iect Screen Ided on all iress and signals. Iect Screen Ided on all iress and signals. Iect Screen Ided on all iress and signals. Iect Screen Ided on all iress and signals.
DRAM ROW Cycle DRAM WRITE Reco DRAM WRITE Reco DRAM RASH to RA DRAM READ To WR DRAM WRITE To RE DRAM WRITE To RE DRAM WRITE To RE DRAM WRITE To RE DRAM WRITE To RE DD Delay (SD) 5 [Auto] DRAM WRITE To WRITE Timing 4 [Auto] DRAM READ To READ Timing 4 [Auto] DRAM READ To READ Timing 4 [Auto] DRAM REF Cycle Time 110 [Auto] DRAM Refresh Rate 7.8 [Auto]	Ided on all Incess and signals. Iect Screen Ided on all Incess and signals. Iect Screen Incess and Signals. Iect Screen Incess and Signals. Incess and Signals. Incess and Signals. Incess and Signals. Incess and Signals. Incess and Signals. Incess and Signals.
DRAM ROW Cycle DRAM WRITE Reco DRAM WRITE Reco DRAM RASH to RA DRAM READ To WR DRAM WRITE To RE DRAM WRITE To RE DRAM WRITE To RE DRAM WRITE To RE DRAM WRITE To WRITE Timing 4 [Auto] DRAM WRITE To WRITE Timing 4 [Auto] DRAM READ To READ Timing 4 [Auto] DRAM REF Cycle Time 110 [Auto] DRAM Refresh Rate 7.8 [Auto] DRAM Command Rate 117 [117]	Ided on all Ided on all Incess and signals. Iect Screen It Sclect Item It Change Option F1 General Help F10 Save and Exit ESC Exit
DRAM ROW Cycle DRAM WRITE Reco DRAM RASH to RA DRAM READ To UR DRAM WRITE To R DRAM WRITE To R DRAM WRITE To READ Delay (SD) 5 [Auto] DRAM WRITE To WRITE Timing 4 [Auto] DRAM WRITE To WRITE Timing 4 [Auto] DRAM READ To READ Timing 4 [Auto] DRAM REF Cycle Time 110 [Auto] DRAM Refresh Rate 7.8 [Auto] DRAM Command Rate 11 [11]	Ided on all it now? Ided on all iress and signals. Iect Screen F1 Select Item F1 General Help F10 Save and Exit ESC Exit

#### Test result?

In order to demonstrate the performance and stability of PRINCO DDR3-1800 DIMM board, We use the strictest stress testing, that is, multi-core MemTest in window 7.

(Data rate : 900.0\*2=1800, timing : 8, 9, 8, 27, multi-core test => pass!)



### Advanced Overclocking and Testing

### Part II: Heavy test

If you want to know the potential of PRINCO DDR3-1800? Following are step-by-step howto.

#### How to use?

- 0. Clear BIOS to mainboard initial setting
- 1. Enter BIOS setup and [Ai Tweaker] menu
- 2. Enter [Ai Overclock Tuner] item and select [Manual]
- 3. Select [CPU/HT Reference Clock (MHz)] item <sup>,</sup> and increase to higher Base clock rate (ex:243). Then select [DRAM Frequency] item <sup>,</sup> and set the DDR3 memory to higher clock rate (ex:1944MHz). Don't forget setting [CPU Ratio] item to suitable ratio (ex:14.0)

(In this case we only focus on memory over clocking, not CPU)

4. Select[DRAM Voltage] item , and set the value to [1.65000]

Step 1 : Select " Ai Tweak	er."			
Step 2 : CPU Overclocking			Set	[Manual]
Step 3 : CPU/HT Reference	e Clock (MHz)		Set [	[243]
Step 4 : DRAM Frequency			Set 1	1944MHz1
Step 5 : CPU Ratio			Set I	14.01
Step 6 : DRAM Voltage			Cot 1	1 65000 1
Step 0. Divin Voltage			Sec (	llongion 1404
Hain Bi Tureaken B	duanced Poue	Root	Tools	Fxit
Step 1			10010	
CPU OverClocking	[Manual]	Step 2		
OC From CPU Level Up	[Auto]		Min	= 1.200000
CPU Ratio	[14.0] 5	itep 5	Max	= 2.445000(*)
CPU/HT Reference Clock	k (MHz) [243] S	tep 3	Inte	rval = 0.015000
PCIE Frequency	[100]		Stan	dard = 1.500000
DRAM Frequency	[1944MH:	2 Step 4	+/-	: Raise/Reduce
CPU/NB Frequency	[Auto]			
HT Link Speed	[Auto]			
GPU Booster	Enabled	13		
Enhanced iGPU SpeedSt	ep [Auto]			
GPU Power Saving Mode	Disable	ed]		
OC Tuner Utility				
DRAM Tining configuration	tion		4-3	Select Screen
▶ DRAM Driving Configura	ation		11	Select Item
******* Please key in m	umbers directly	******	F1	General Help
CPU & NB Voltage Mode	[Offset]	L.	F10	Save and Exit
CPU Offset Voltage	1.400 [Auto]		ESC	Exit
CPU/NB Offset Voltage	1.350 [Auto]			
CPU UDDA Voltage	2.500 [Auto]	-		
DRAM Voltage	1.650 11.65000	Step 6	*	
v02.61 (C) Cop	yright 1985-2010	J, American	Megatren	ds, Inc.

## 5. Enter [DRAM Timing Configuration] item

Main	Ai Tweaker	M4A88TD Advanced	V EVO/USB3 Power	BIOS Set Boot	tup Tot	ols	Version 1404 Exit
OC Fro	m CPU Level Ho		[Auto]				
CPU Ra	tio		[14.0]				
CPU/	HT Reference Clo	ock (MHz)	[243]				
PCIE F	requency		[100]				
DRAM F	requency		[1944MHz]				
CPU/NB	Frequency		[Auto]				
HT Lin	k Speed		[Auto]				
GPU Bo	oster		[Enabled]				
Enha	nced iGPU Speeds	Step	[Auto]				
GPU	Power Saving Mod	le	[Disabled]				
OC Tun	er Utility						
▶ DRAM	Timing configu	ration					
► DRAM	Driving Configu	iration				4-9	Select Screen
*****	* Please key in	numbers d	lirectly! **	*****		11	Select Item
CPU &	NB Voltage Mode	1 100	WffsetJ			Enter	Go to Sub Screen
CPU	Uffset Voltage	1.400	IAutol			FI	General Help
CPU/	MB Uffset Volta	Je 1.358	IAutol			F10	Save and Exit
CPU UD	DH Voltage	2.500	Hutoj			LSC	EXIT
UKHI U	ortage	1.000	11.05000				
HI Vol	tage	1.200	Hutol				

#### 6. set [DRAM CAS# Latency] item to [9 CLK]

set [DRAM RAS# to CAS# Delay] item to [9 CLK]

set [DRAM RAS# PRE Time] item to [9 CLK]

set [DRAM RAS# ACT Time] item to [27 CLK]

set [DRAM Command Rate] item to [1T]

then return to previous to [Ai Overclock Tuner] menu

Version 1404 ASH Latency
AS# Latency
Select Screen Select Item Change Option General Help Ø Save and Exit C Exit

### 7. Save BIOS changes [F10] and exit

Ai Tweaker	A88TD-V EVO/USB	3 BIOS Setup		Version 1404
DRAM Timing configuration			CAS	Latency
DRAM CASH Latency DRAM RASH to CASH Delay DRAM RASH PRE Time	9 19 CLKI 9 <b>19 CLKI</b> 9 <b>19 CLKI</b>			
DRAM RAS# ACT Time DRAM READ to PR	27 [27 CLK]			٦.
DRAM WRITE Reco Save con	nfiguration cha	inges and exit	now?	
DRAM WRITE Reco DRAM RASH to RA DRAM READ TO UR DRAM WRITE TO R	nfiguration cha [[0k]	inges and exit [Cancel]	now?	lact Screen
DRAM WRITE Reco DRAM RASH to RA DRAM READ To VR DRAM WRITE To R DRAM WRITE To READ Delay(C DRAM WRITE To WRITE Timing	nfiguration cha [Ok] SD) 6 [Auto]	inges and exit [Cancel]	now?	lect Screen
DRAM WRITE Reco DRAM WRITE Reco DRAM RASH to RA DRAM READ To WR DRAM WRITE To R DRAM WRITE To READ DelayC DRAM WRITE To WRITE Timing DRAM READ To READ Timing DRAM READ To READ Timing	nfiguration cha (Ok) SD) 6 [Auto] g 4 [Auto] 3 [Auto]	inges and exit [Cance]]	now?	Lect Screen Select Item Change Option General Help Saug and Exit

#### Test result?

We use the strictest stress testing , multi-core MemTest in

window 7 , to show you PRINCO DDR3-1800 potential.

(Data rate : 972.0\*2=1944, timing : 9, 9, 9, 27, multi-core test =>

pass!)

